29) **MMX extensions, data types and operating principles**

**Extensions**

Eight new 64-bit data registers, called MMX registers

Three new packed data types:

— 64-bit packed byte integers (signed and unsigned)

— 64-bit packed word integers (signed and unsigned)

— 64-bit packed doubleword integers (signed and unsigned)

Instructions that support the new data types and to handle MMX state

Management

Extensions to the CPUID instruction



